



PATENT

IFW

Applicant(s): Jerrell Hein

Title: RECONFIGURABLE TERMINAL

Application No.: 10/675,529

Filed: September 30, 2003

Examiner: Not yet assigned

Group Art Unit: 2182

Atty. Docket No.: 026-0036

October 4, 2004

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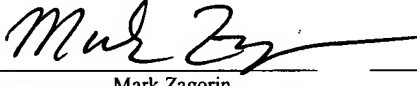
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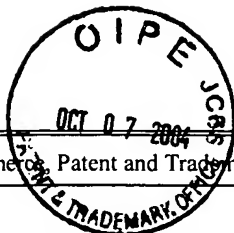
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Respectfully submitted,



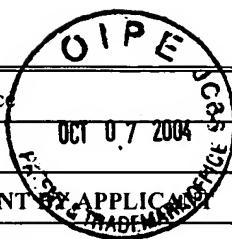
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U.S. Department of Commerce Patent and Trademark Office				Attorney Docket No.: 026-0036		
				Application No.: 10/675,529		
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<b>U.S. Patent Documents</b>						
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					<b>Translation</b>	
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<b>Examiner</b>			<b>Date Considered</b>			
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	AYY	Giles, T. G., "A Universal Frequency Synthesizer IC," Philips Telecommunication Review, Vol. 37, No. 3, August 1979, pages 177-181.
	AZZ	Goto, Junichi, et al., "A Programmable Clock Generator With 50 to 350 MHz Lock Range For Video Signal Processors," <u>IEEE 1993 Custom Integrated Circuits Conference</u> , pages 4.4.1 - 4.4.2.
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Examiner

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